

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

LOW-DOPED BASE SPACER FOR REDUCTION OF Emitter BASE CAPACITANCE IN BIPOLAR TRANSISTORS WITH SELECTIVELY GROWN EPITAXIAL BASE

the specification of which (check one) is attached hereto or was filed on _____ as Application No. _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)		<u>Priority Claimed</u>
		<u>Yes</u> <u>No</u>

_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
Number	Country	Day/Month/Year Filed	

_____	_____	<input type="checkbox"/>	<input type="checkbox"/>
Number	Country	Day/Month/Year Filed	

I hereby claim the benefit under 35 U.S.C. § 119(3) of any United States provisional application(s) below:

Application Number	Filing Date
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Application Number	Filing Date
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Application Number	Filing Date
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I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Filing Date	Status: Patented, Pending, Abandoned
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Application Number	Filing Date	Status: Patented, Pending, Abandoned
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Application Number	Filing Date	Status: Patented, Pending, Abandoned
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor Alexei Sadovnikov

Inventor's signature Alexei Sadovnikov

June 18, 2001

Date

Residence 924 Redwood Ave, Sunnyvale, CA 94084

Citizenship Canadian

Post Office Address 924 Redwood Ave, Sunnyvale, CA 94084

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Alexei Sadovnikov

Appln. No.: 09/885,462

Filed: June 20, 2001

For: LOW DOPED BASED SPACER FOR
REDUCTION OF Emitter-BASE
CAPACITANCE IN BIPOLAR
TRANSISTORS WITH SELECTIVELY
GROWN EPITAXIAL BASE

Group Art: 2814

Examiner: Unknown

POWER OF ATTORNEY
BY ASSIGNEE

Commissioner for Patents
Washington, D.C. 20231

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the above-identified application by assignment dated June 18, 2001, which assignment was recorded in the Patent and Trademark Office on June 20, 2001, at Reel 11931, Frame 261, hereby appoints the members of the firm of STALLMAN & POLLOCK LLP, a firm composed of:

Michael A. Stallman	29,444	Kathleen A. Frost	37,326
Michael J. Pollock	29,098	Brian J. Keating	39,520

121 Spear Street, Suite 290, San Francisco, CA 94105, (415) 512-1312, FAX (415) 512-1362, and the following members of the National Semiconductor Corporation Intellectual Property Department:

Christopher J. Byrne	32,204	Andrew S. Viger	28,552	Coleman F. Reif	38,593
John Maxin	34,668	Eugene C. Conser	39,149	Allen R. Tremain	40,207
Pater Y. Wang	40,452				

as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

STALLMAN & POLLOCK LLP
Attn: Michael J. Pollock
121 Spear Street, Suite 290
San Francisco, CA 94105

Telephone: (415) 512-1312
Facsimile: (415) 512-1362

Dated: 9/21/01

NATIONAL SEMICONDUCTOR CORPORATION

By:

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing



UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Offic
ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

SEPTEMBER 06, 2001

PTAS



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STALLMAN & POLLOCK LLP
MICHAEL J. POLLOCK
121 SPEAR STREET, SUITE 290
SAN FRANCISCO, CA 94105

RECEIVED

SEP 12 2001

STALLMAN & POLLOCK LLP

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 06/20/2001

REEL/FRAME: 011931/0261

NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

SADOVNIKOV, ALEXEI

DOC DATE: 06/18/2001

ASSIGNEE:

NATIONAL SEMICONDUCTOR CORPORATION
2900 SEMICONDUCTOR DRIVE
SANTA CLARA, CALIFORNIA 95150

SERIAL NUMBER: 09885462

FILING DATE: 06/20/2001

PATENT NUMBER:

ISSUE DATE:

KIMBERLY WHITE, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS

07-02-2001

Tab settings ➡ ➡ ➡ ➡ ➡

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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Alexei Sadovnikov

2. Name and address of receiving party(ies):

Name: National Semiconductor Corporation

Internal Address: _____

Additional names(s) of conveying party(ies) Yes No

3. Nature of conveyance:

Assignment Merger
 Security Agreement Change of Name
 Other _____

Street Address: 2900 Semiconductor Drive

Execution Date: June 18, 2001

City: Santa Clara State: CA ZIP: 95150

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or registration numbers(s):

If this document is being filed together with a new application, the execution date of the application is: June 18, 2001

A. Patent Application No.(s)

NEW

09/885462

B. Patent No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

6. Total number of applications and patents involved:

Name: Michael J. Pollock

Internal Address: STALLMAN & POLLOCK LLP

7. Total fee (37 CFR 3.41): \$ 40.00

Street Address: 121 Spear Street, Suite 290

 Enclosed - Any excess or insufficiency should be credited or debited to deposit account Authorized to be charged to deposit account

City: San Francisco State: CA ZIP: 94105

8. Deposit account number:

50-1703

07/02/2001 LMUELLER 00000074 09885462

DO NOT USE THIS SPACE

019-FC-5A1 Statement and signature. 40.00 0P

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Michael J. Pollock

Michael J. Pollock

Name of Person Signing

Signature

2

Date

Total number of pages including cover sheet, attachments, and document:

ASSIGNMENT

WHEREAS, I, Alexei Sadovnikov, hereinafter referred to as "ASSIGNORS", have invented certain new and useful improvements as described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: LOW-DOPED BASE SPACER FOR REDUCTION OF Emitter BASE CAPACITANCE IN BIPOLAR TRANSISTORS WITH SELECTIVELY GROWN EPITAXIAL BASE

Application (Declaration/Oath) Execution Date: _____

Filing Date: _____ Application No.: _____

WHEREAS, National Semiconductor Corporation, a corporation of the State of Delaware, 2900 Semiconductor Drive, Santa Clara, CA 95051 hereinafter referred to as "ASSIGNEE", is desirous of acquiring the entire right, title and interest in the said invention and application and in any Letters Patent which may be granted on the same;

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN: Be it known that, for One Dollar (\$1.00) and other good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have sold, assigned and transferred, and by these presents do sell, assign and transfer unto the said Assignee, and Assignee's successors and assigns, all right, title and interest in and to the said invention, said application for United States Letters Patent, and any Letters Patent which may hereafter be granted on the same in the United States and all countries throughout the world including any divisions, renewals, continuations in whole or in part, substitutions, conversions, reissues, prolongations or extensions thereof, said interest to be held and enjoyed by said Assignee as fully and exclusively as it would have been held and enjoyed by said Assignors had this assignment and transfer not been made, to the full end and term of any such Letters Patent.

Assignors further agree that they will, without charge to said Assignee, but at Assignee's expense, cooperate with Assignee in the prosecution of said application and/or applications, execute, verify, acknowledge and deliver all such further papers, including applications for Letters Patent and for the reissue thereof, and instruments of assignment and transfer thereof, and will perform such other acts as Assignee lawfully may request, to obtain or maintain Letters Patent for said invention and improvement in any and all countries, and to vest title thereto in said Assignee, or Assignee's successors and assigns.

IN TESTIMONY WHEREOF, Assignor has hereunto signed his/her name to this assignment on the date indicated below.

Date: June 18, 2001

Alex Sad

Alexei Sadovnikov

State of California

County of Santa Clara

On 6-18-01 before me, personally appeared Alexei Sadovnikov, personally known to me or proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

WITNESS my hand and official seal.

Karen L. Metz
Signature of Notary

SEAL

